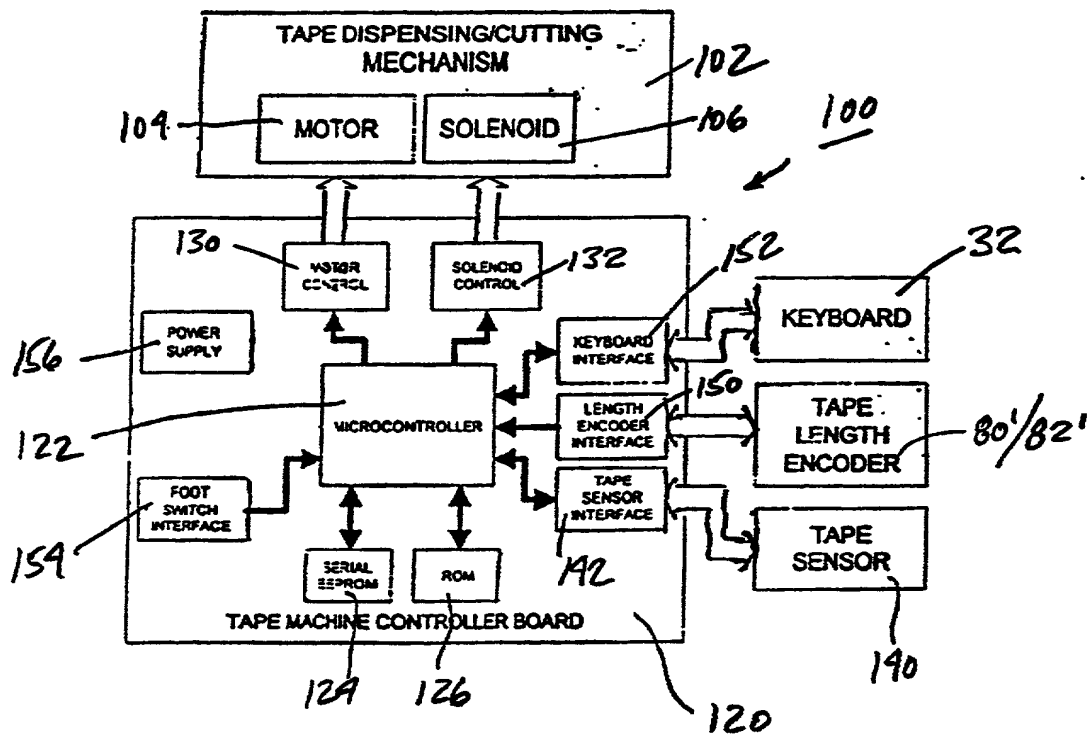


PRIOR ART



PRIOR ART

FIG. 2

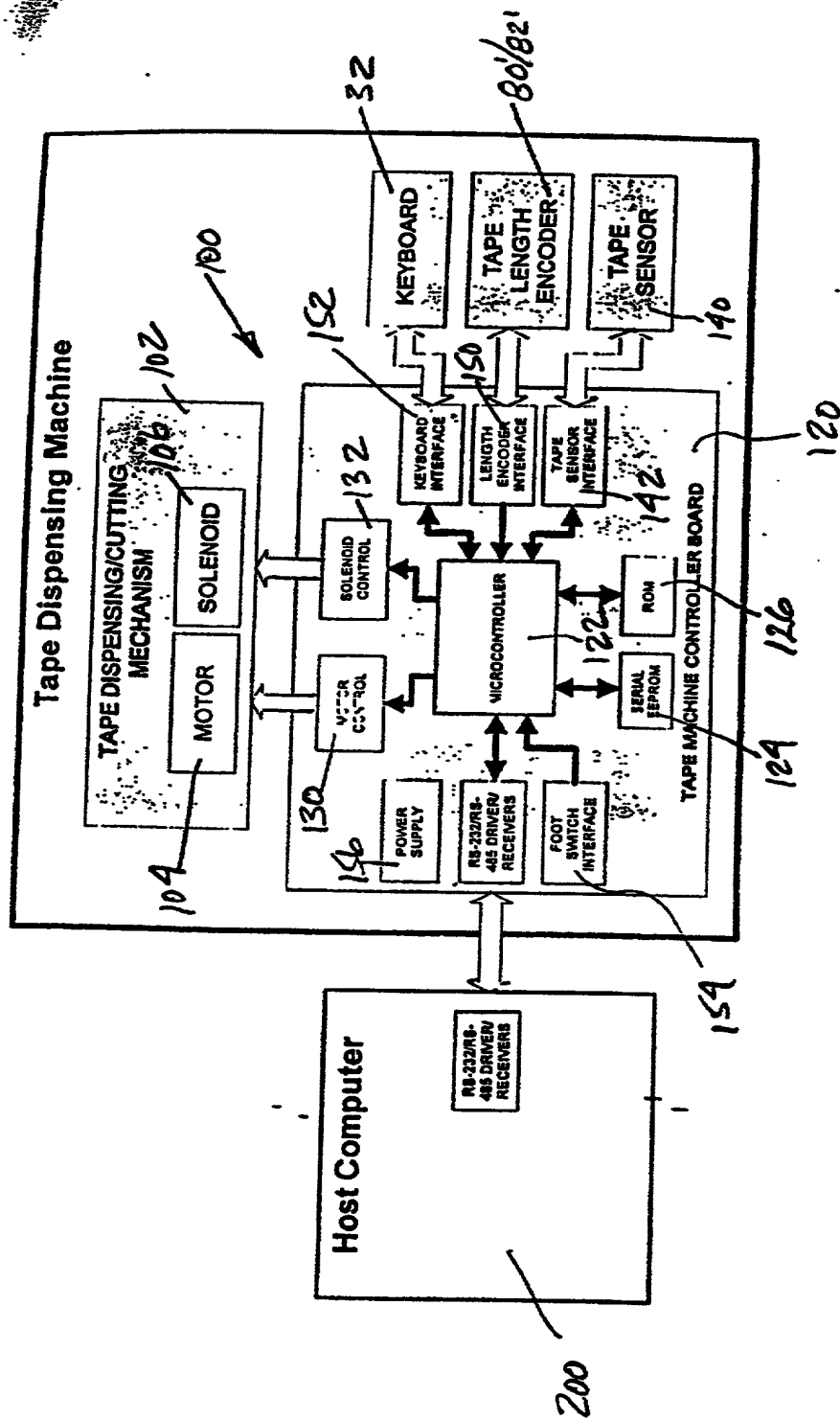


FIG. 3

PRIOR ART

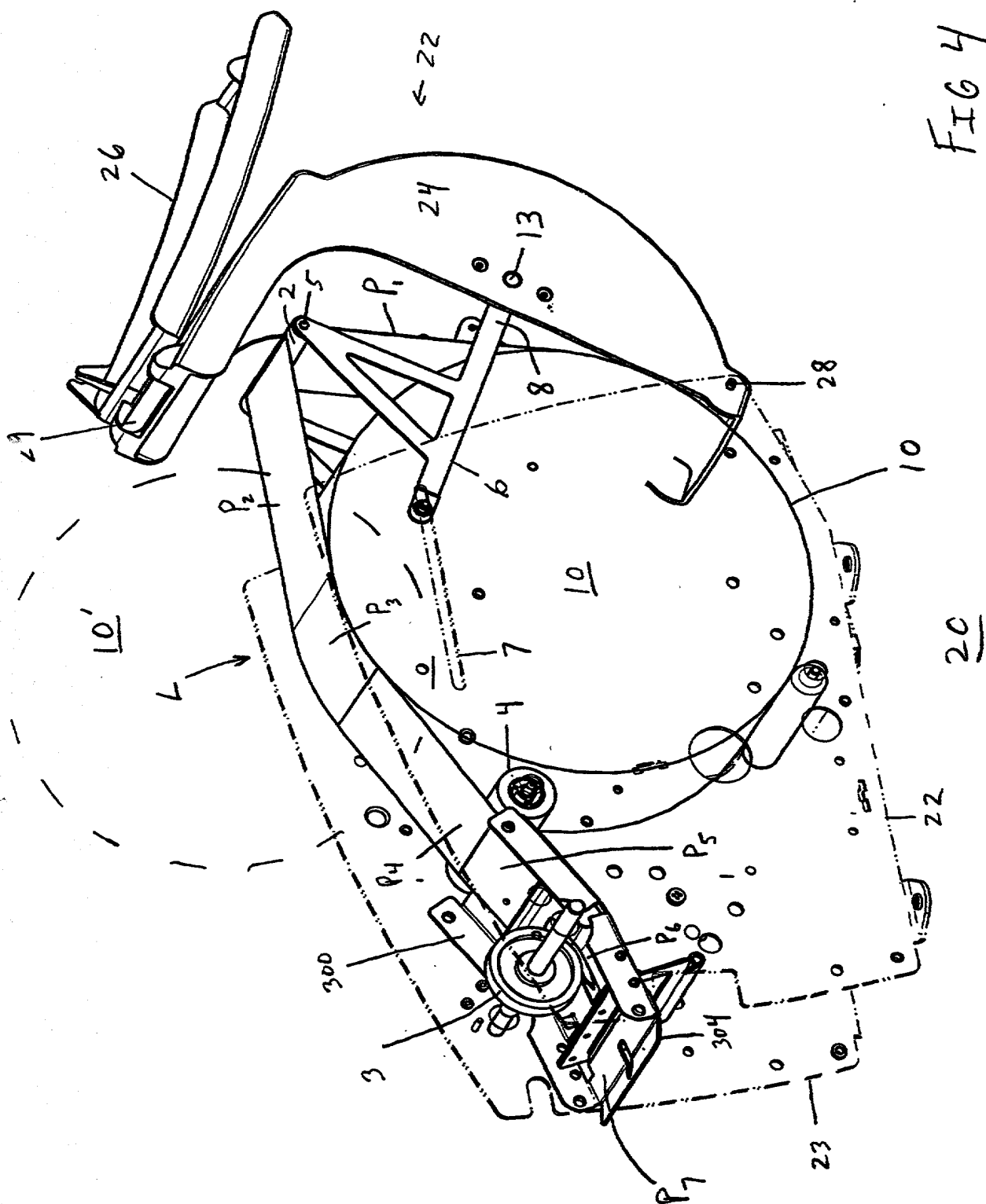


FIG 4

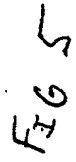
[illegible]

FIG 5

FIG 6A

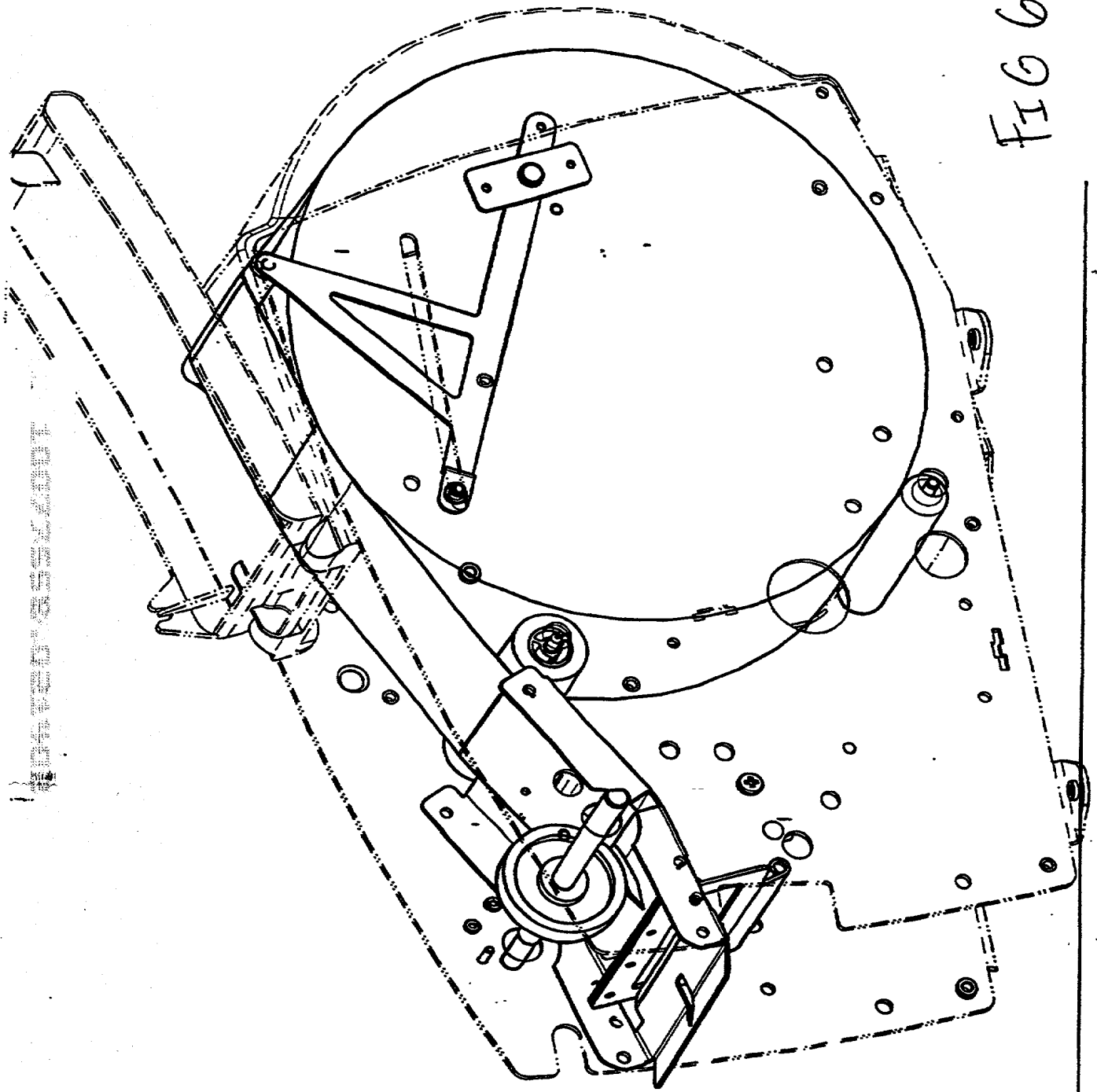


FIG 6B

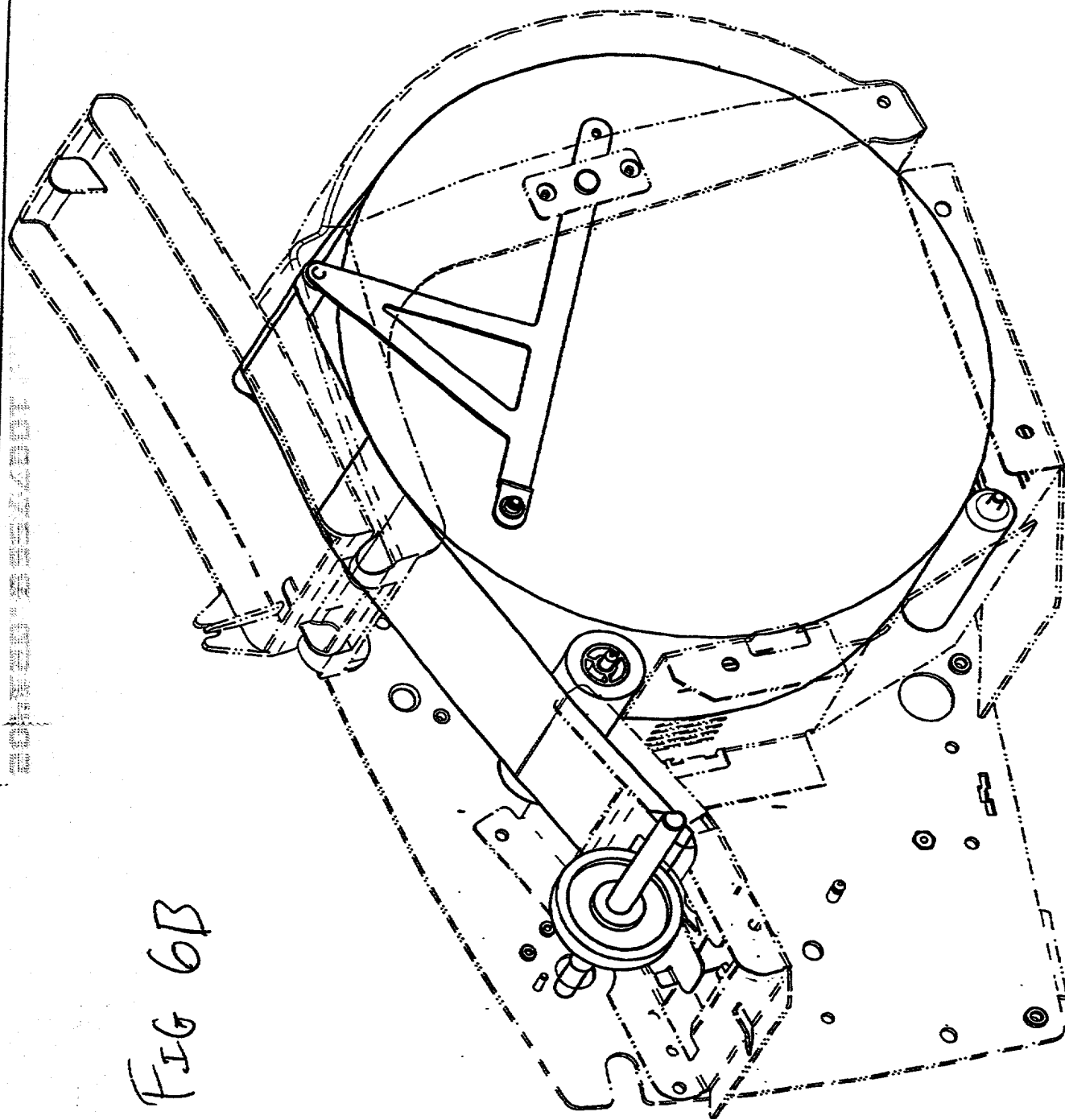


FIG 7

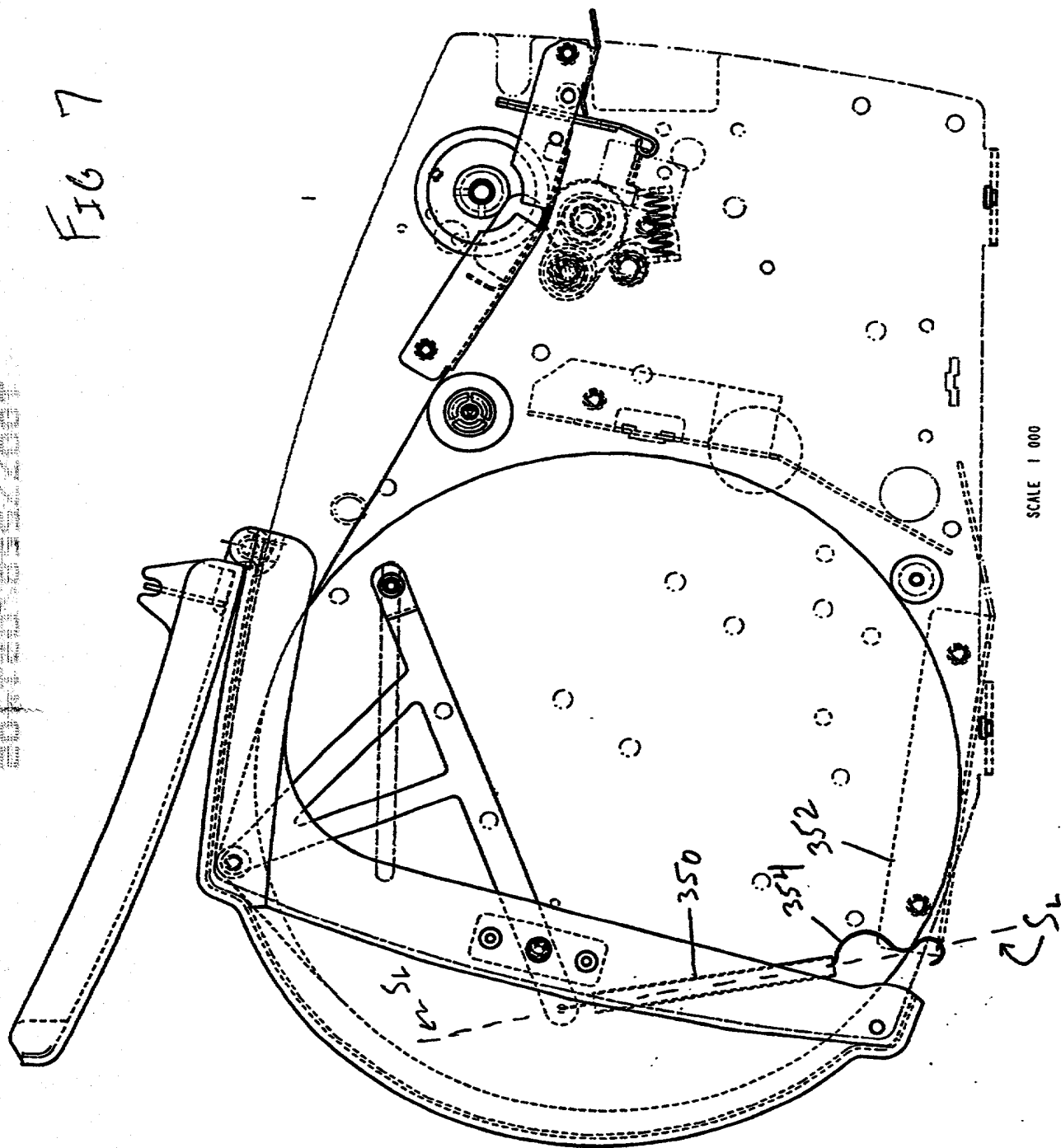
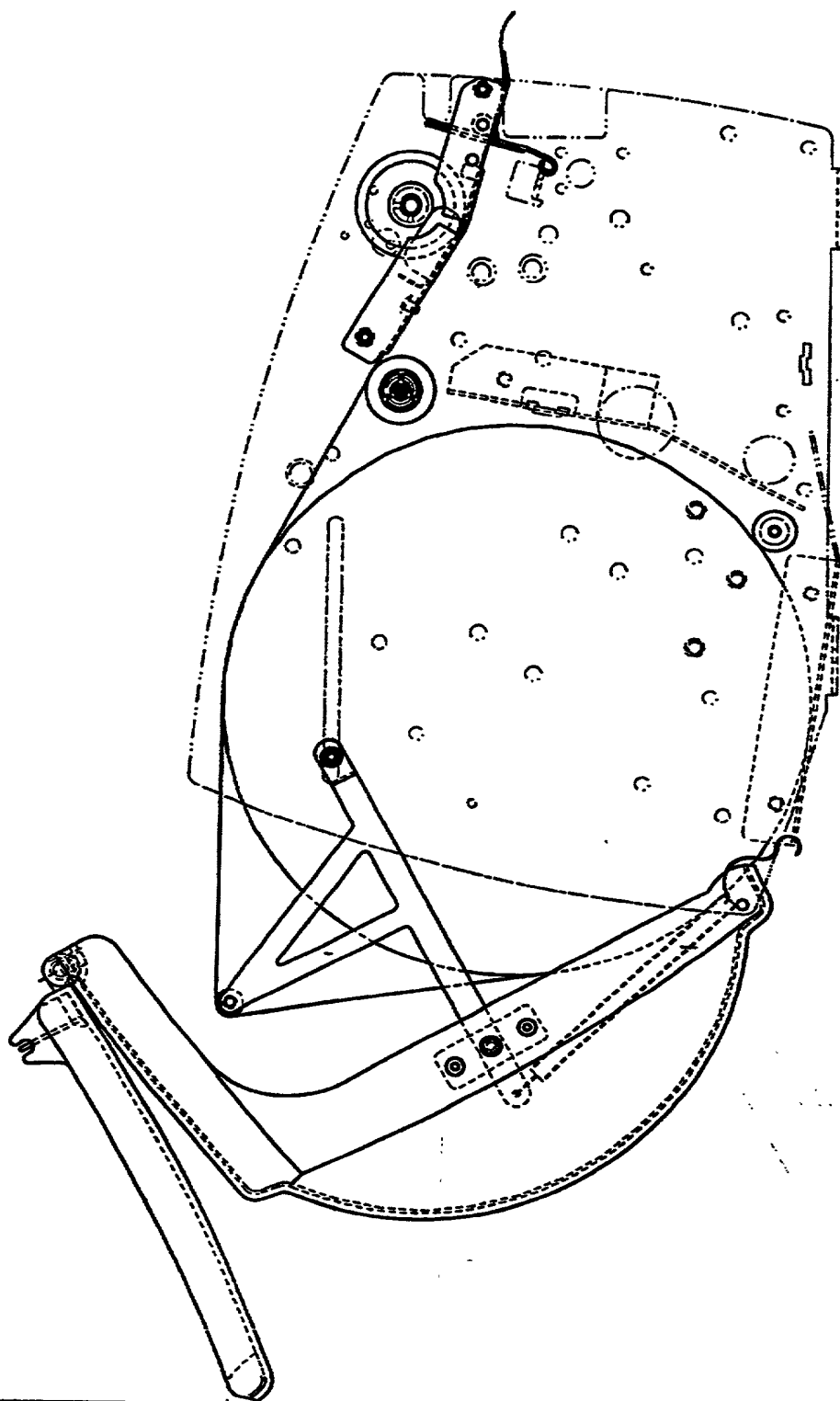


FIG 8



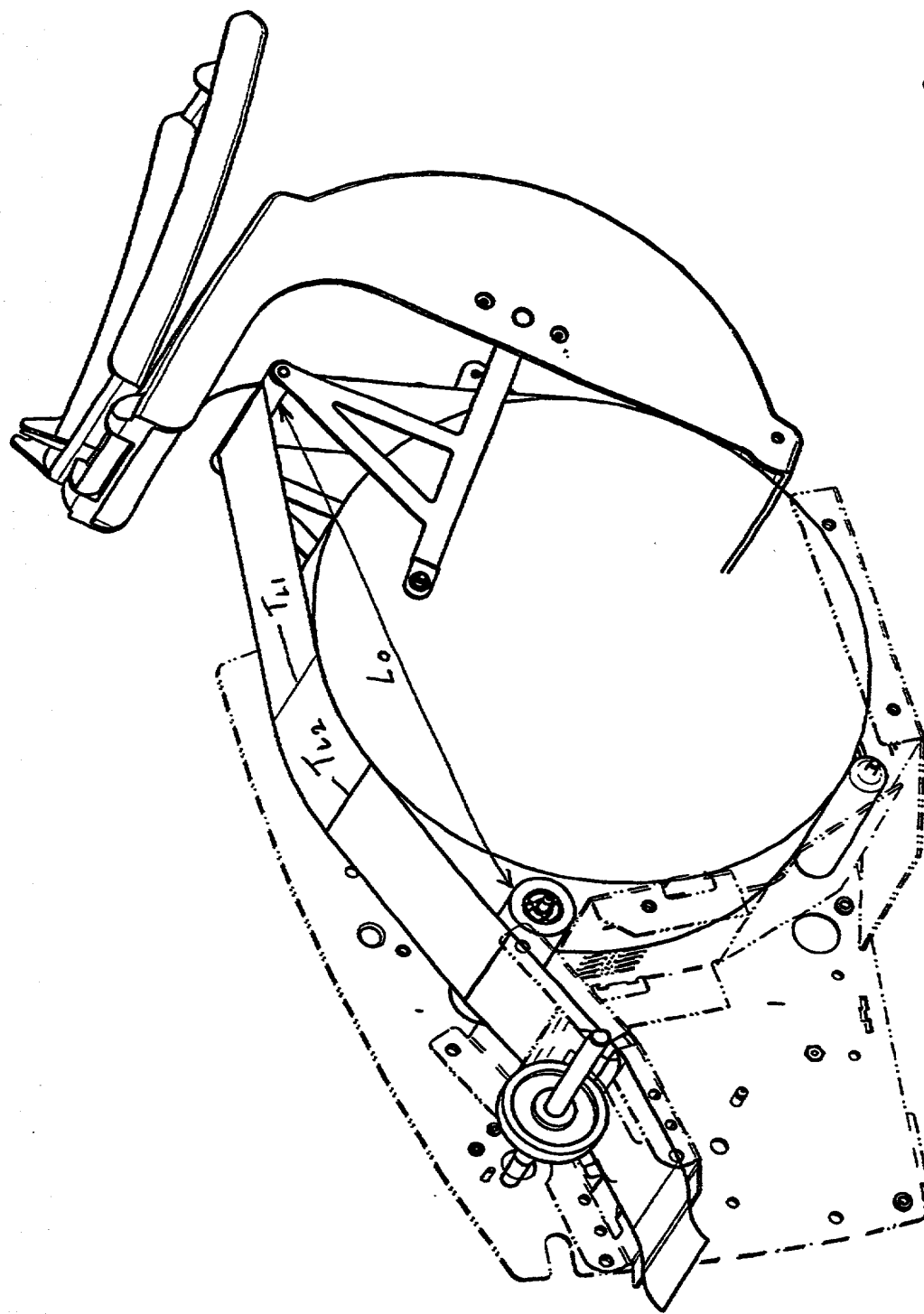


FIG 9

FIG 10

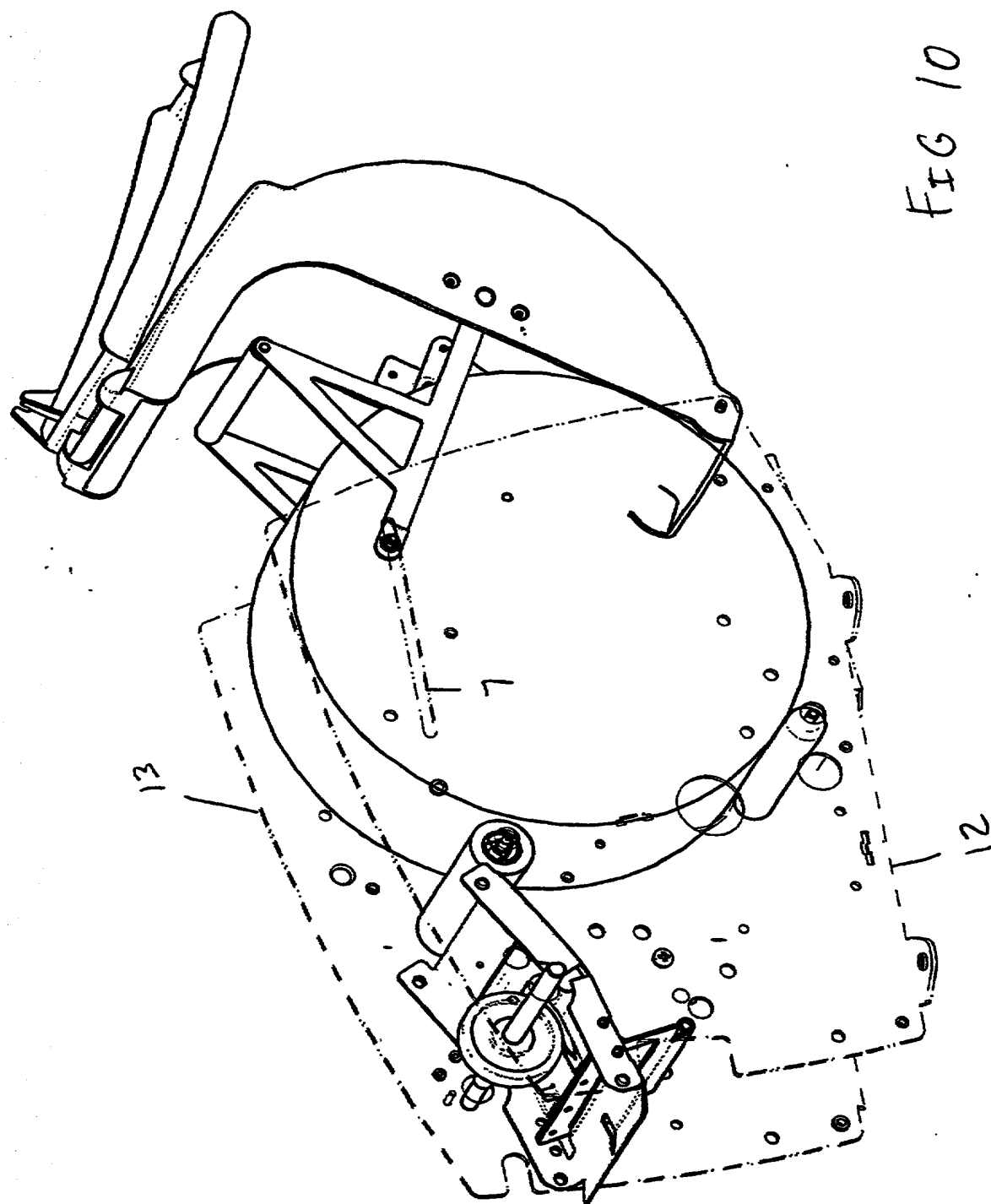


FIG 11

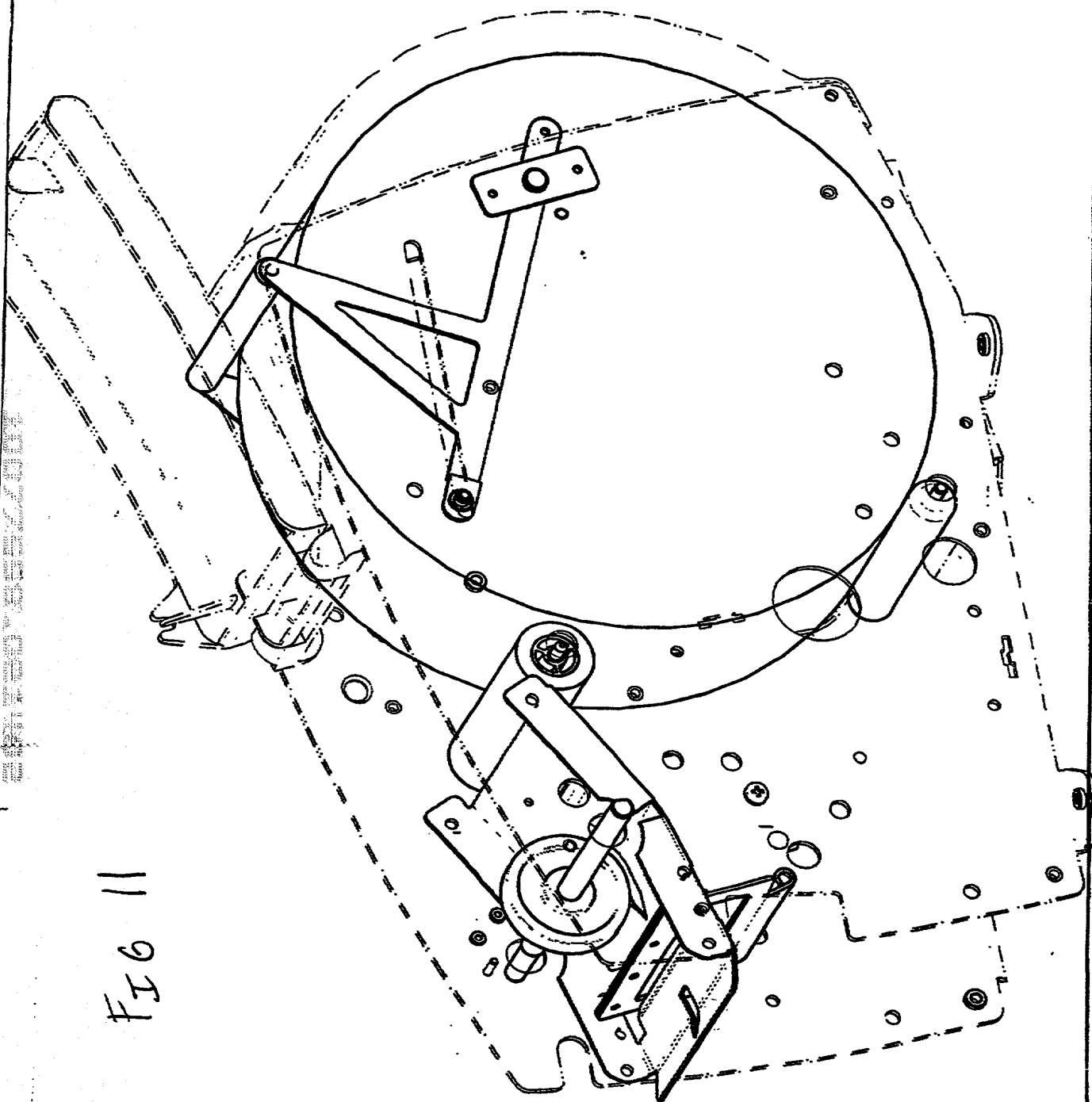


FIG 12

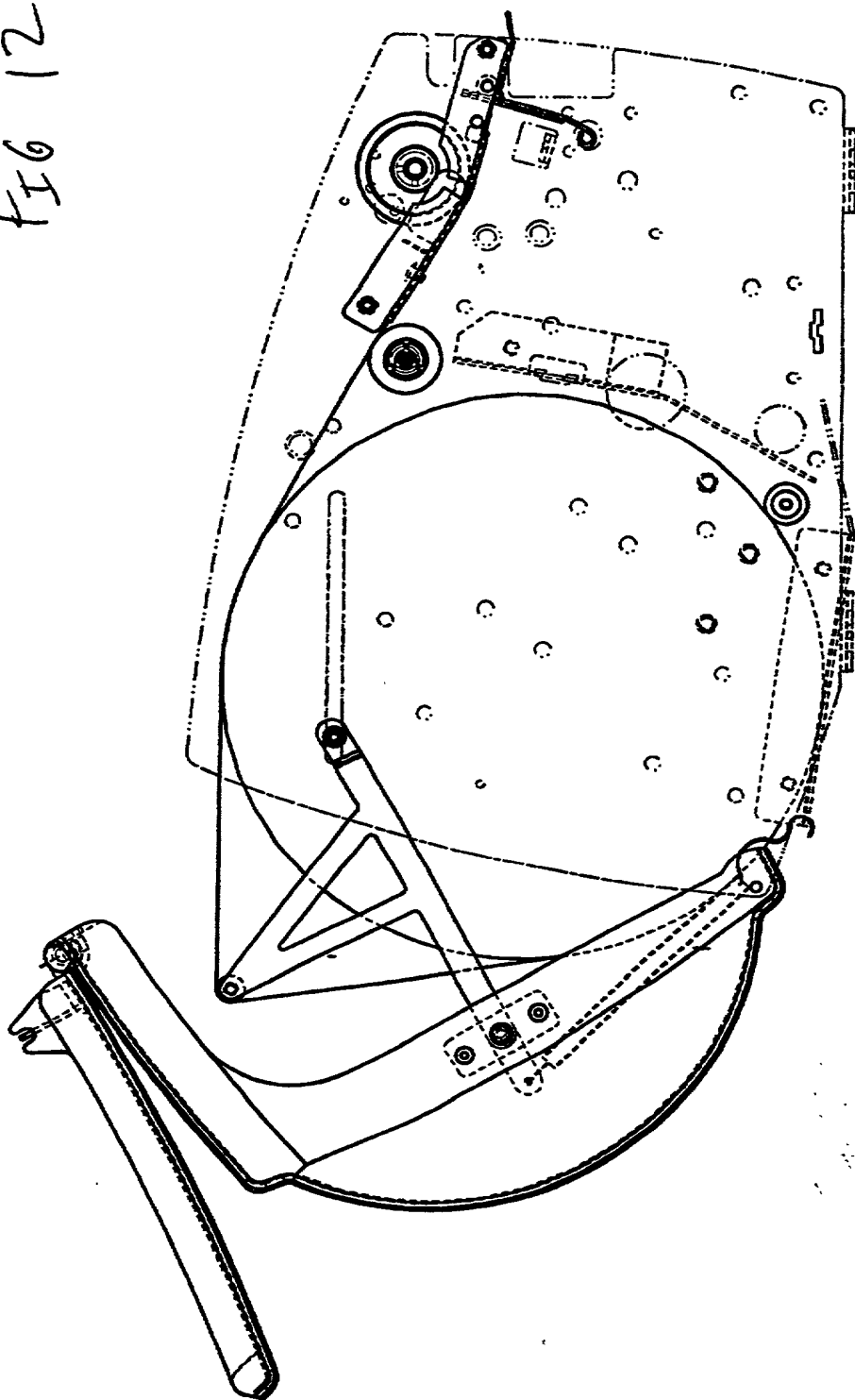
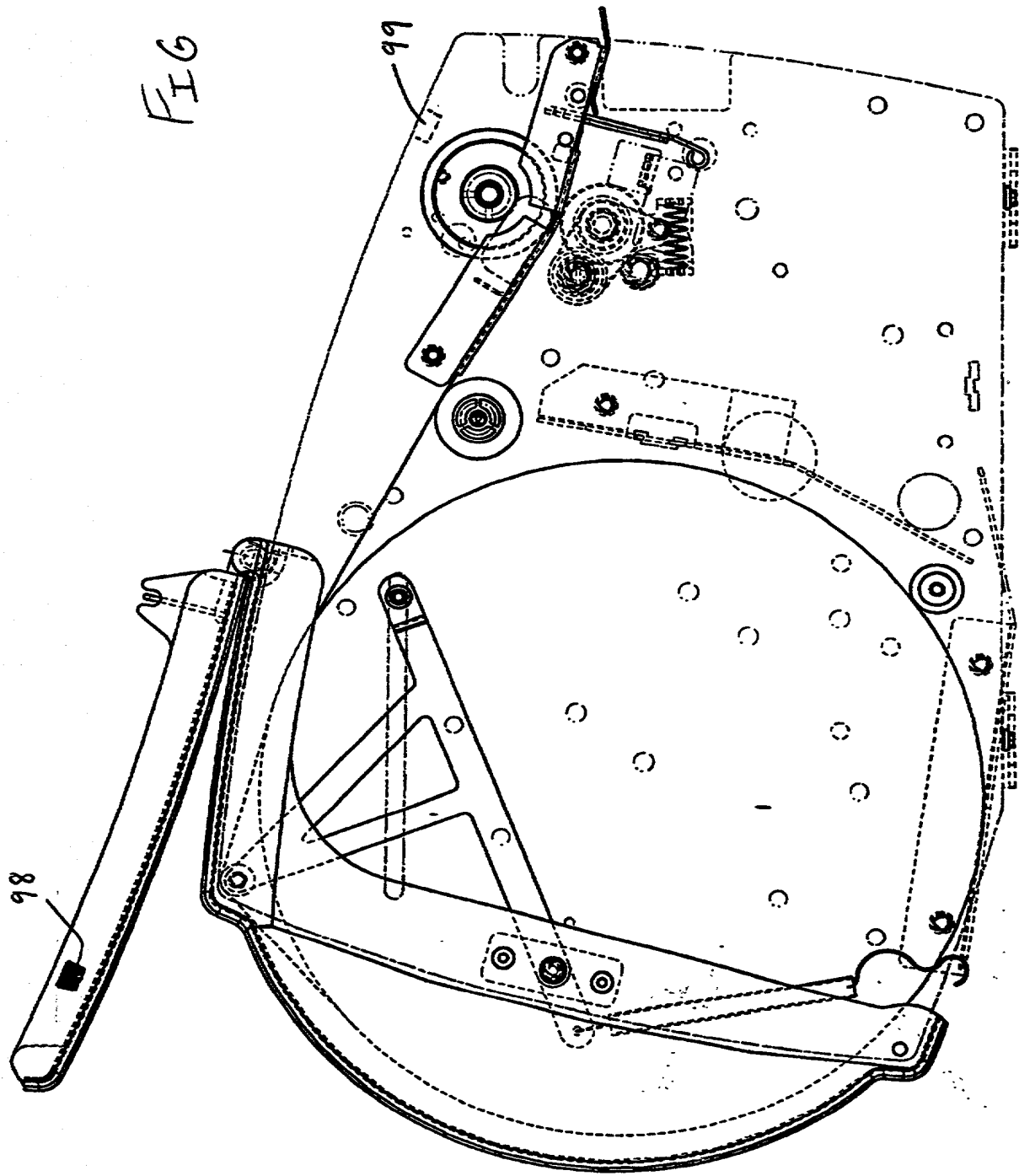


FIG 13



SCALE 1 000

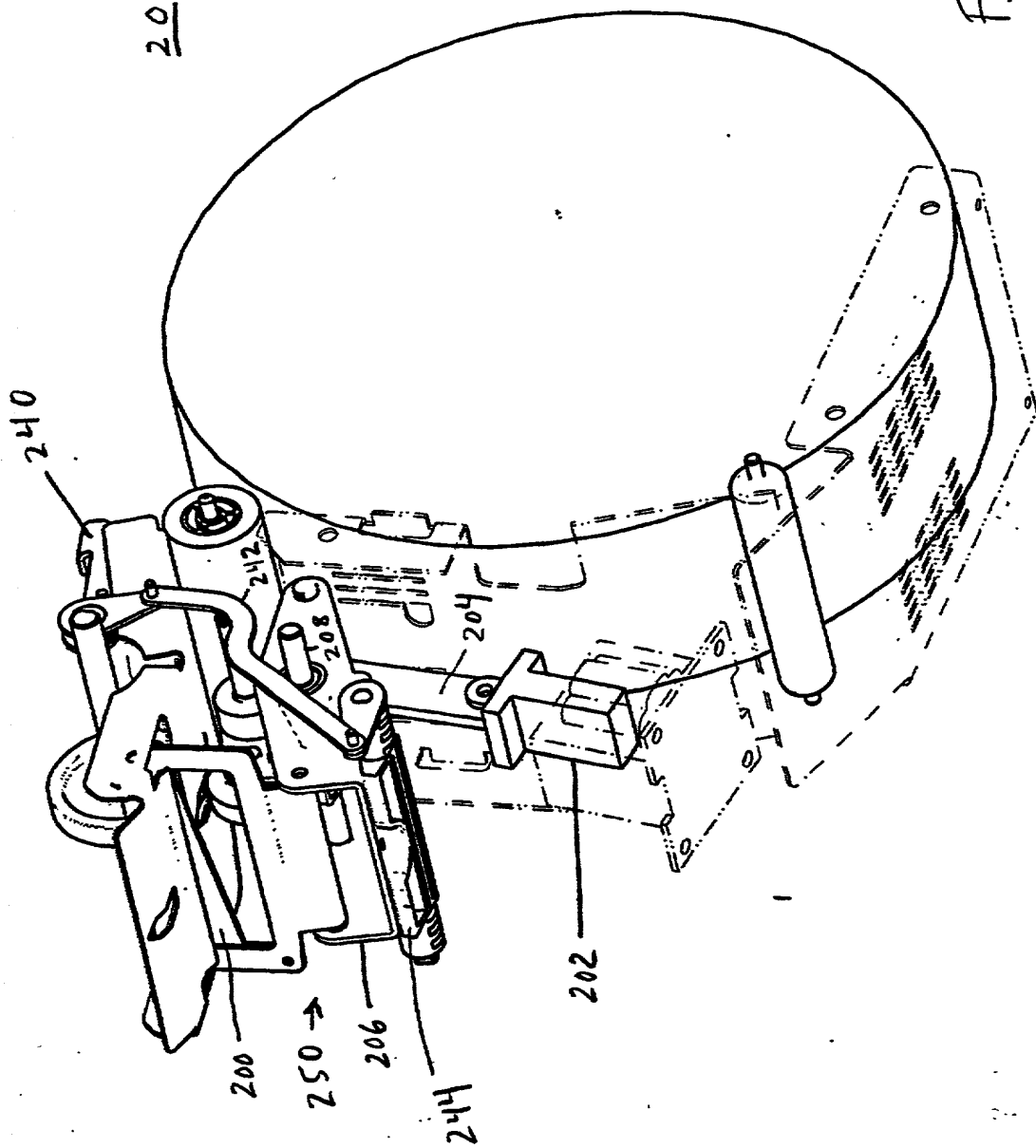
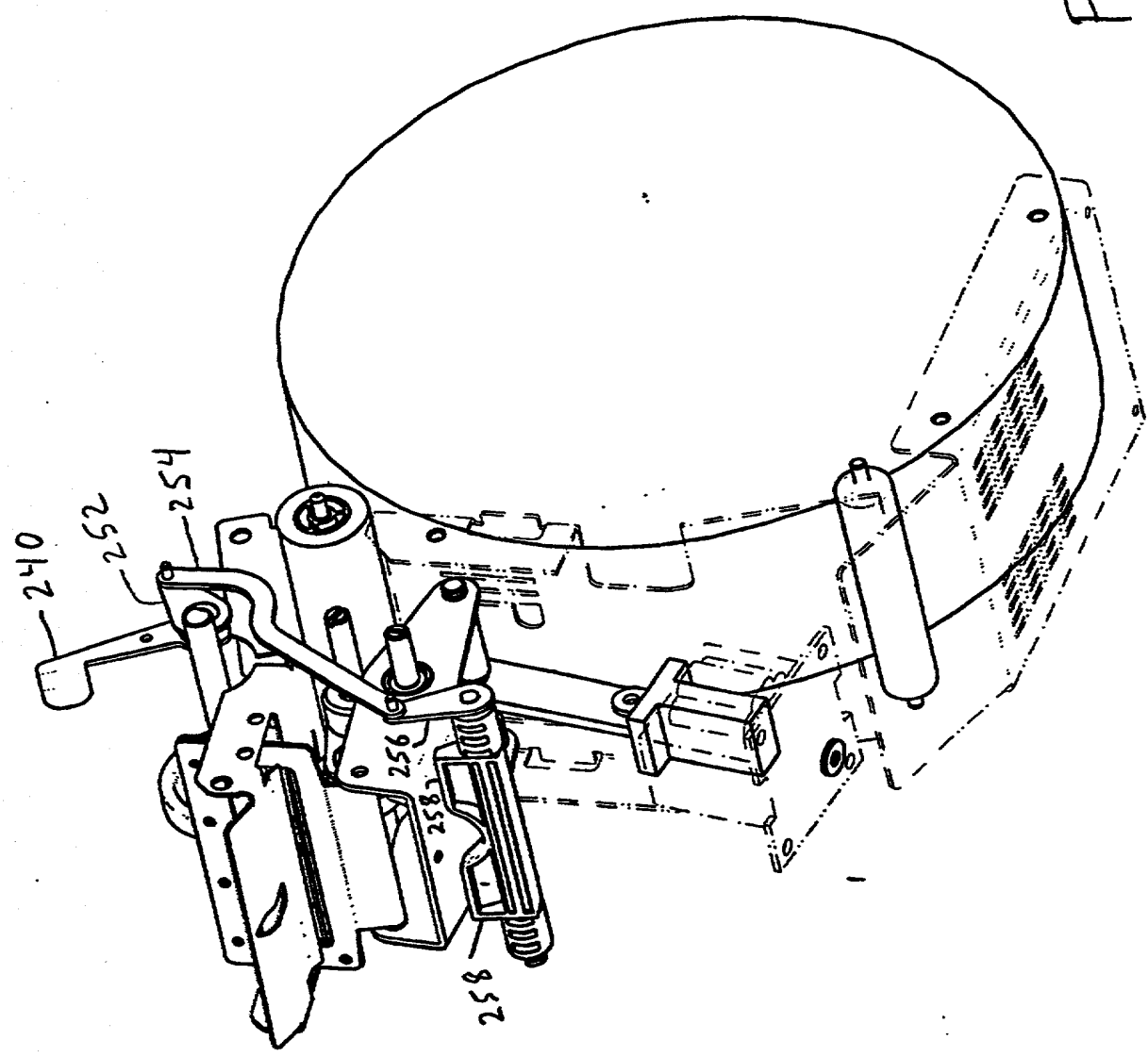


FIG 14

20

FIG 15



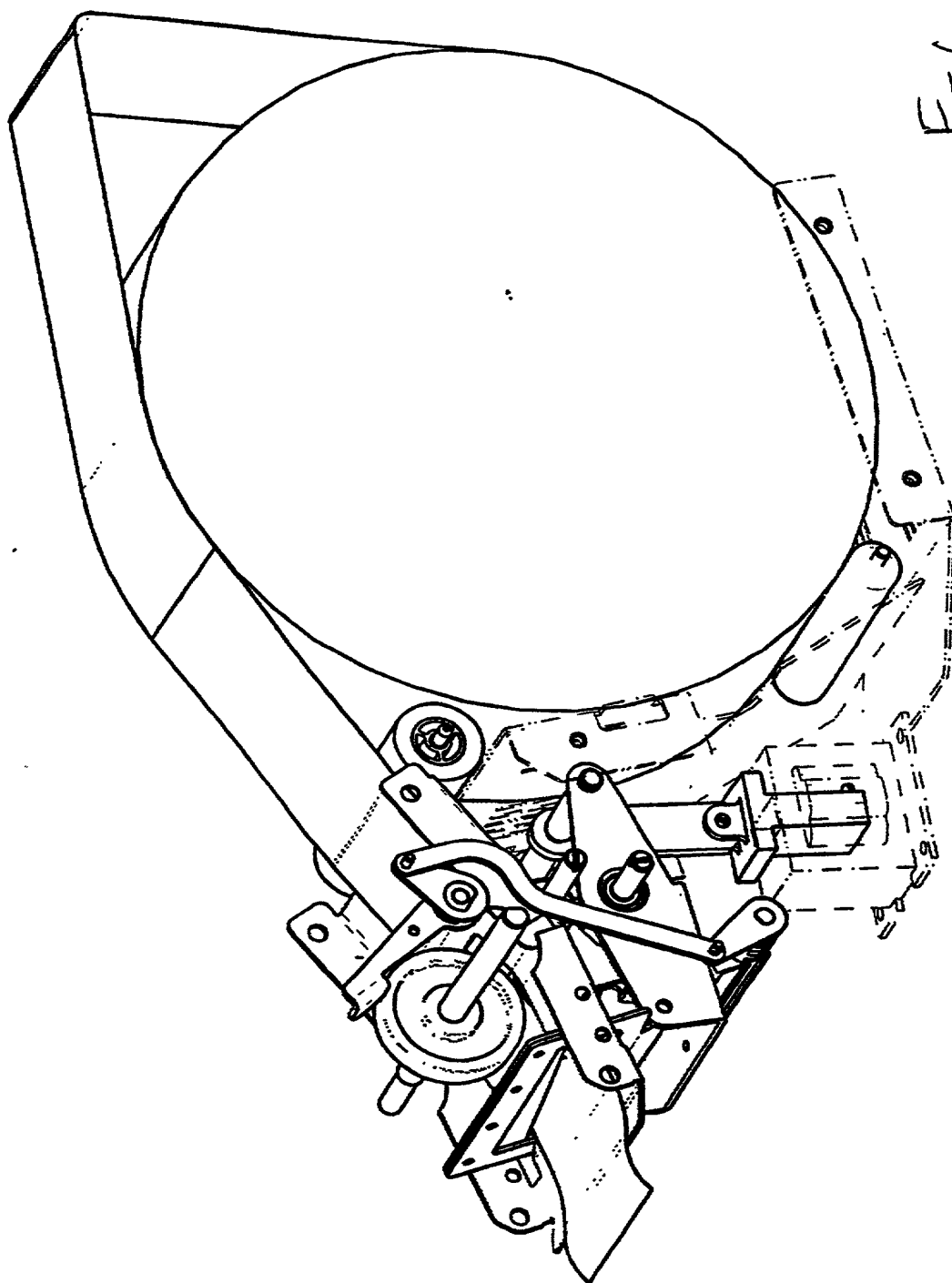
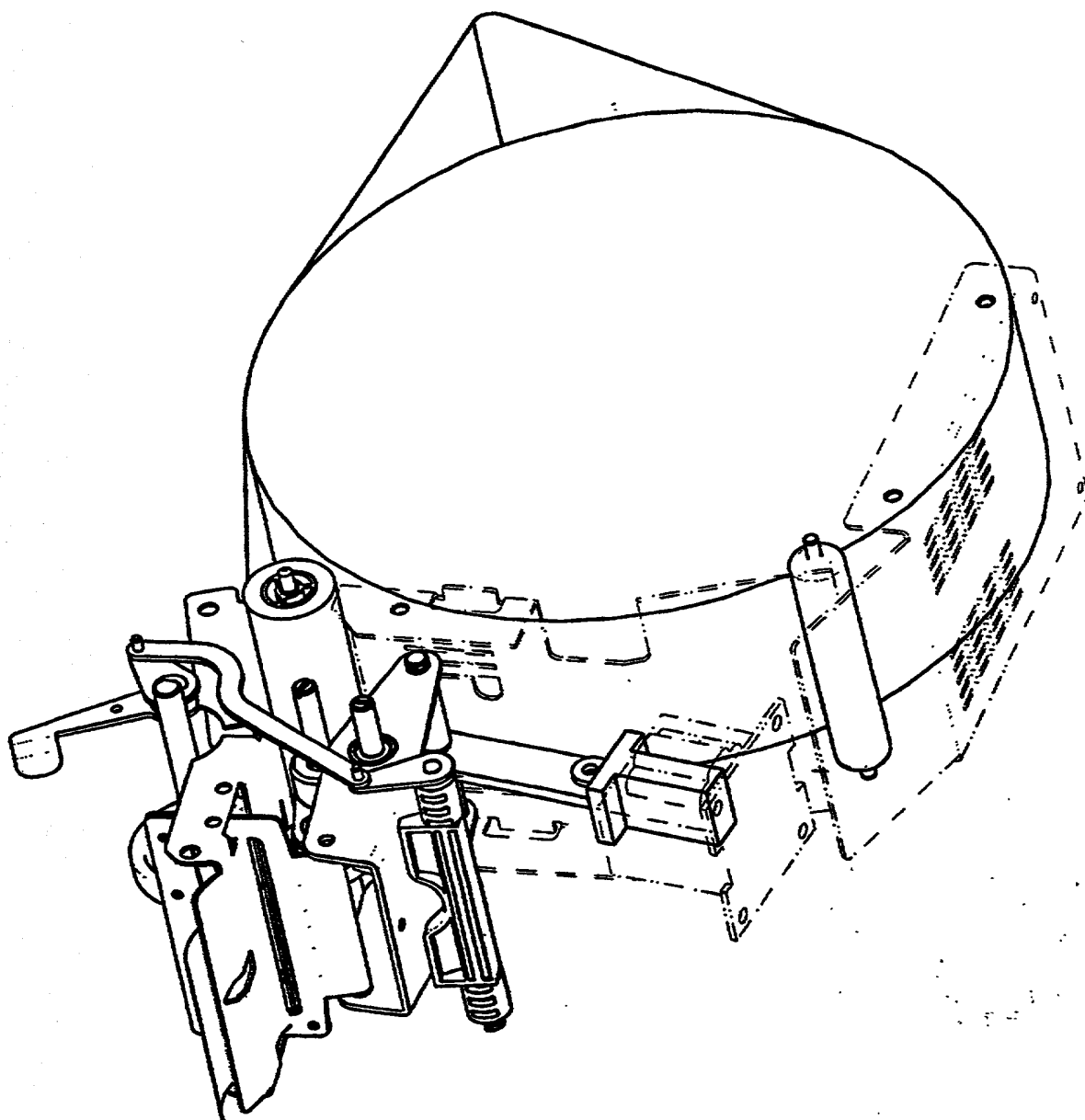
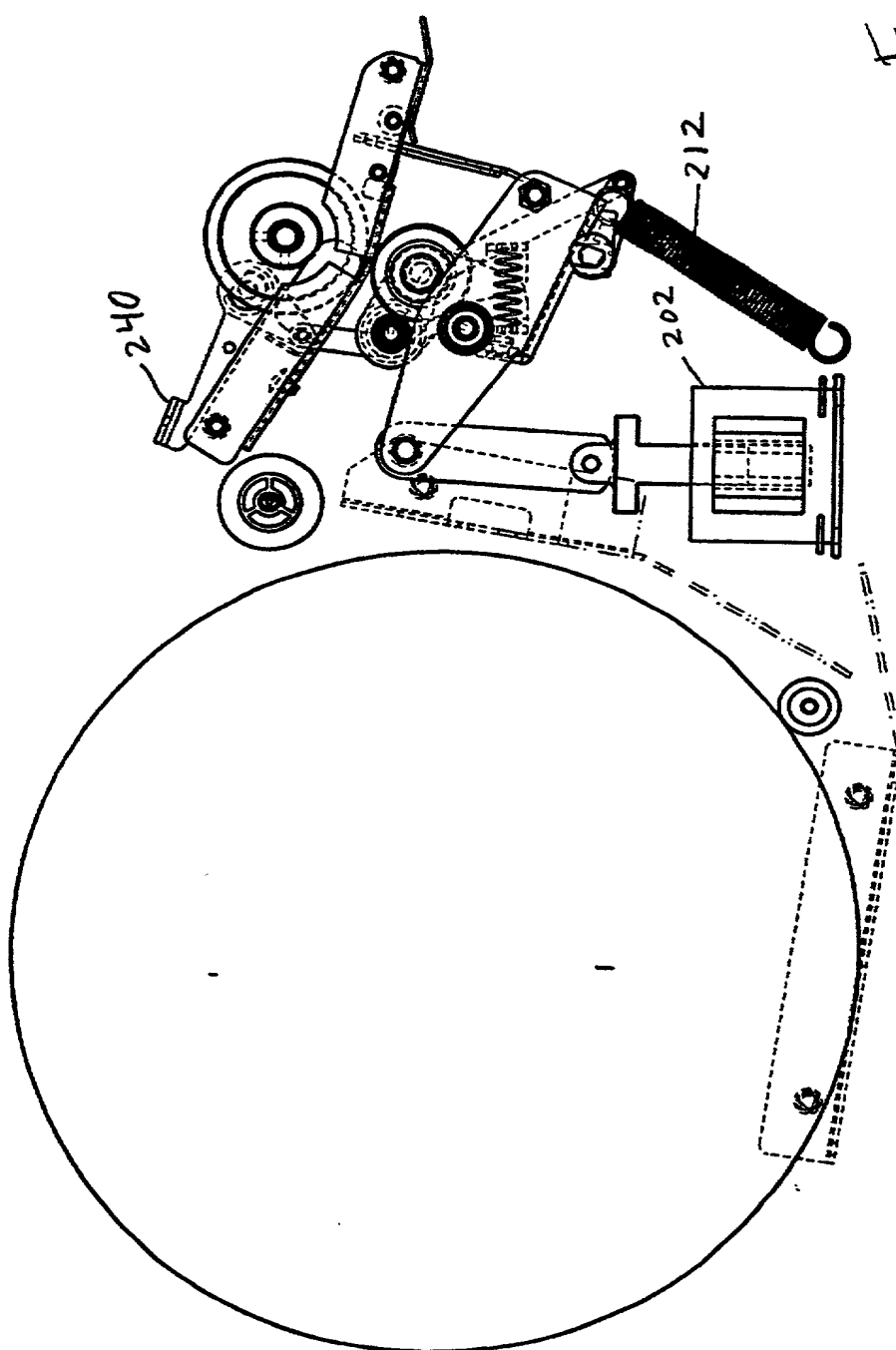


FIG 16

F16 17





SCALE 1.000

Fig 18

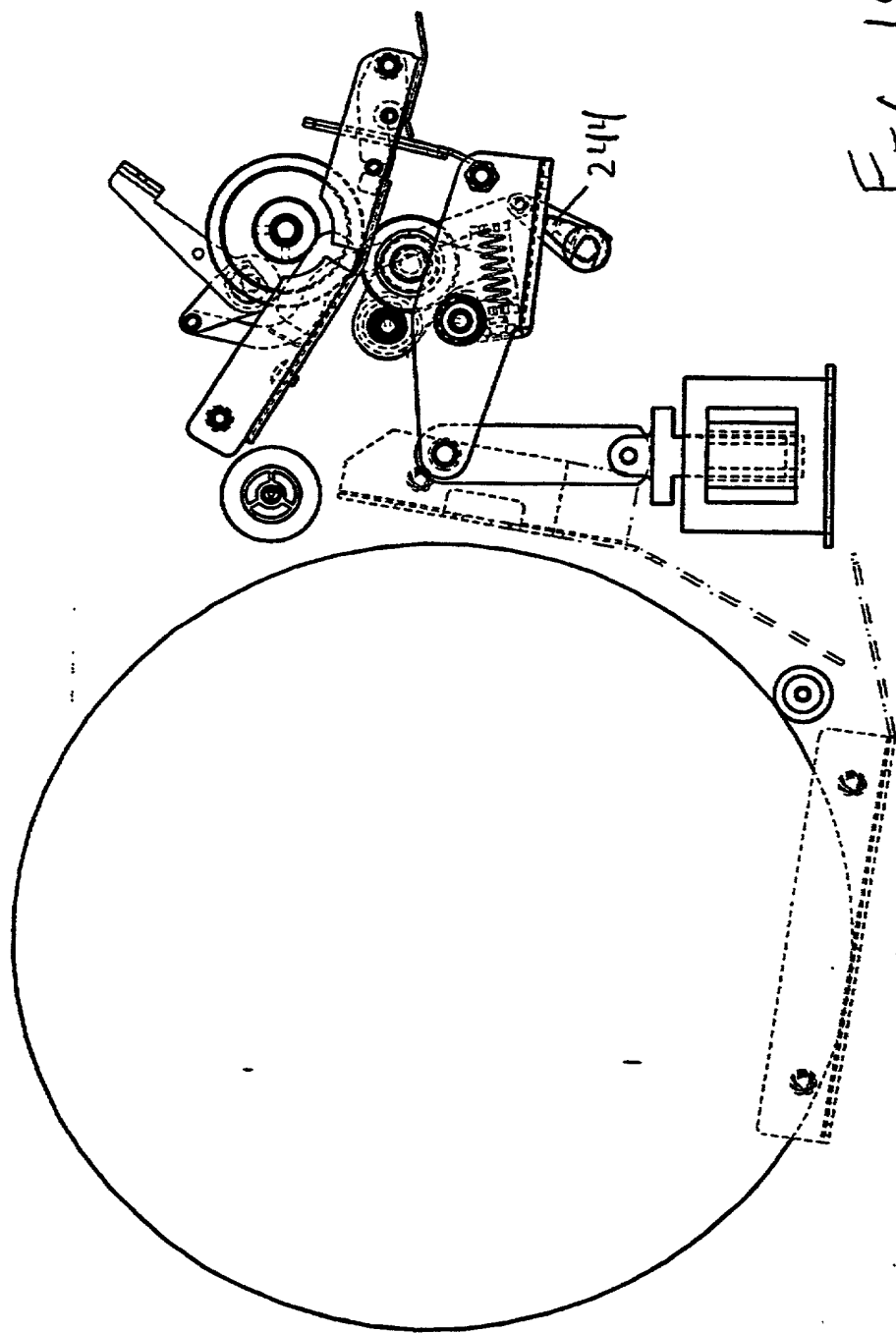
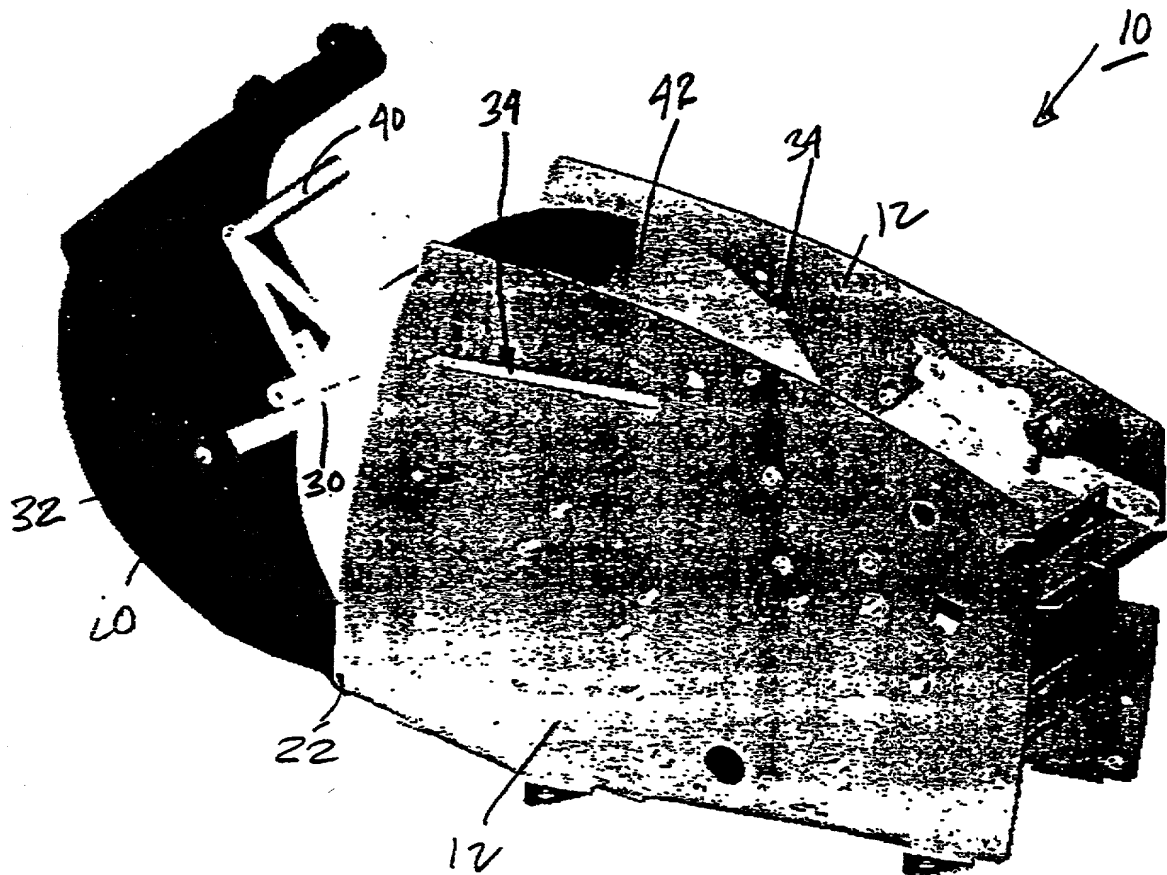
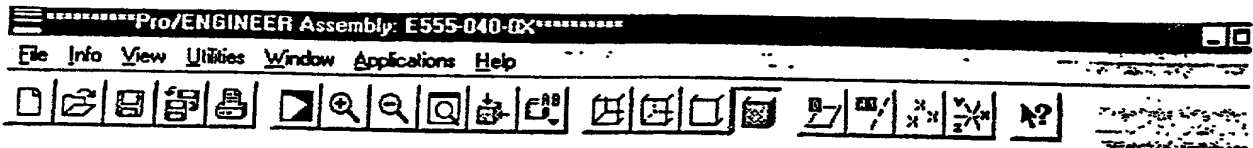


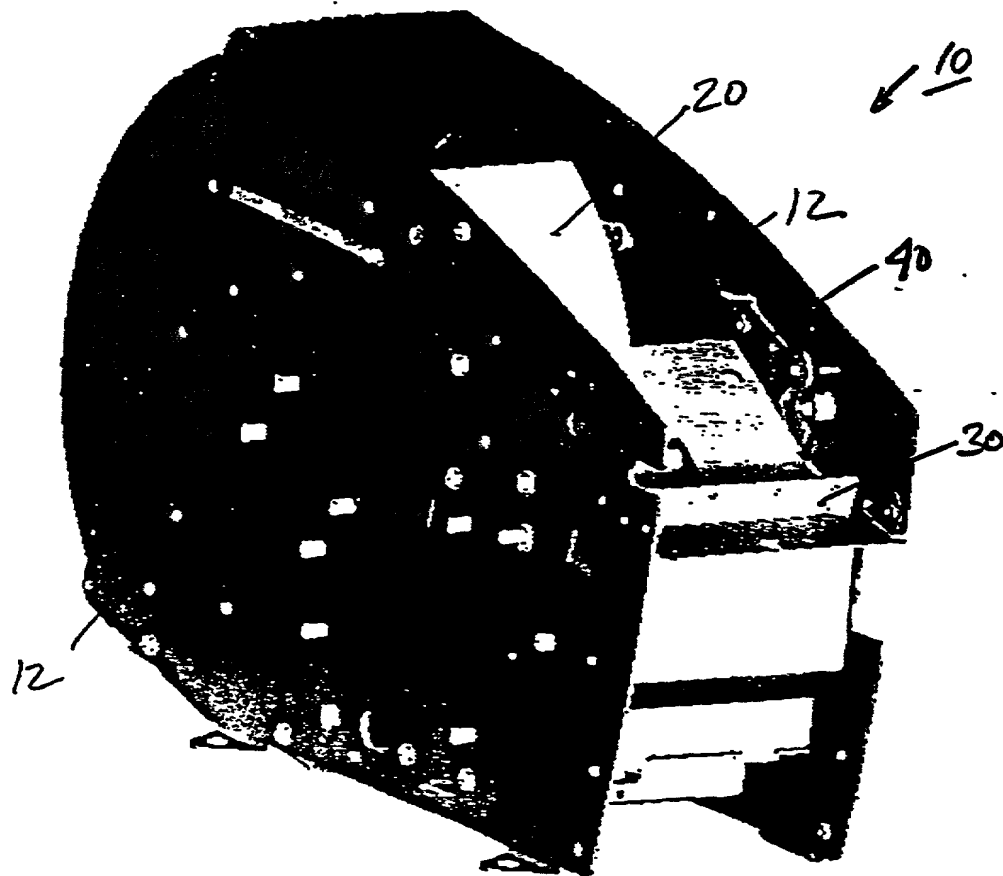
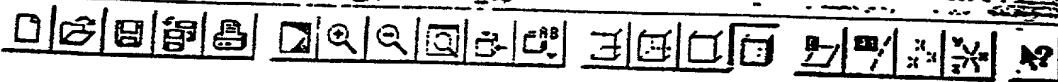
FIG 19

SCALE 1 000



• Processing model E555-090-00 ...
100% has been completed.

FIG 20



• Processing model E555-079-00_...
100% has been completed.

FIG 21

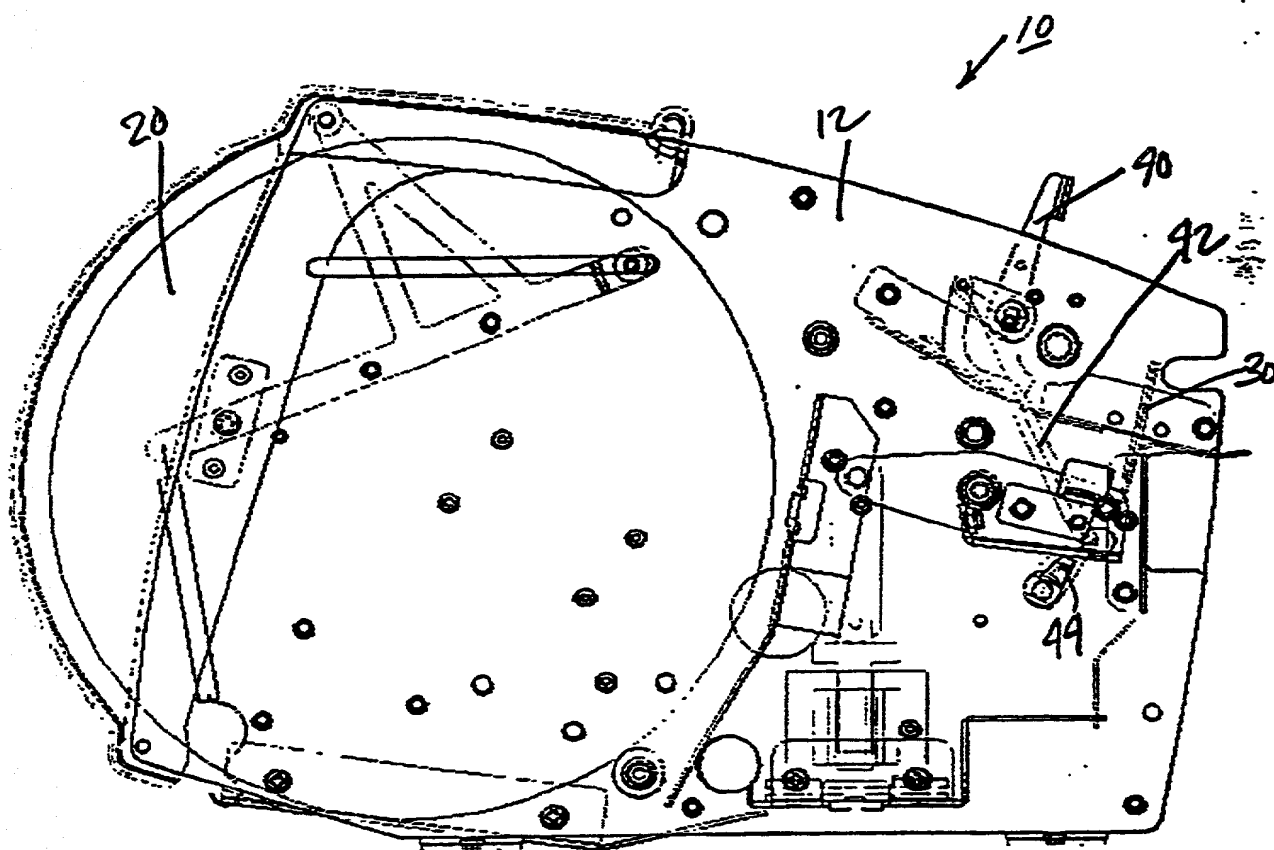


FIG. 22

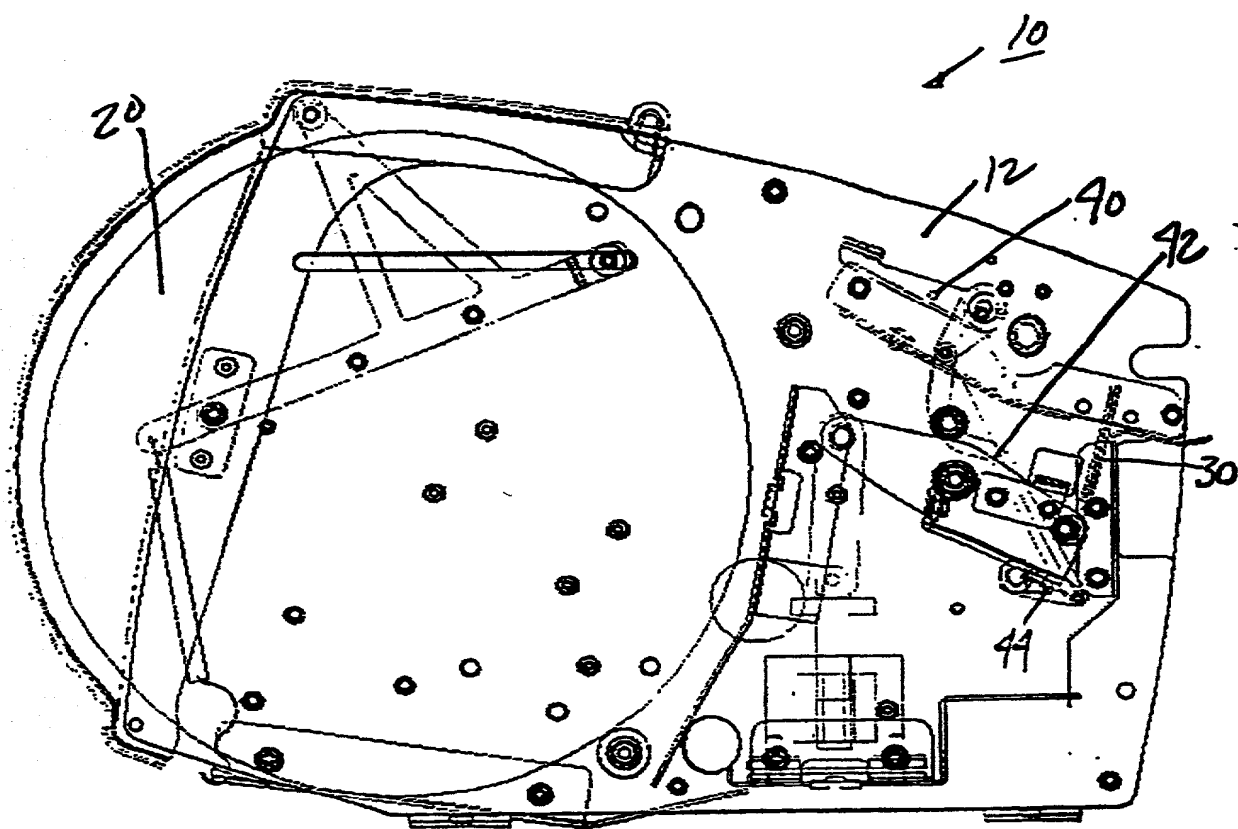


FIG 23

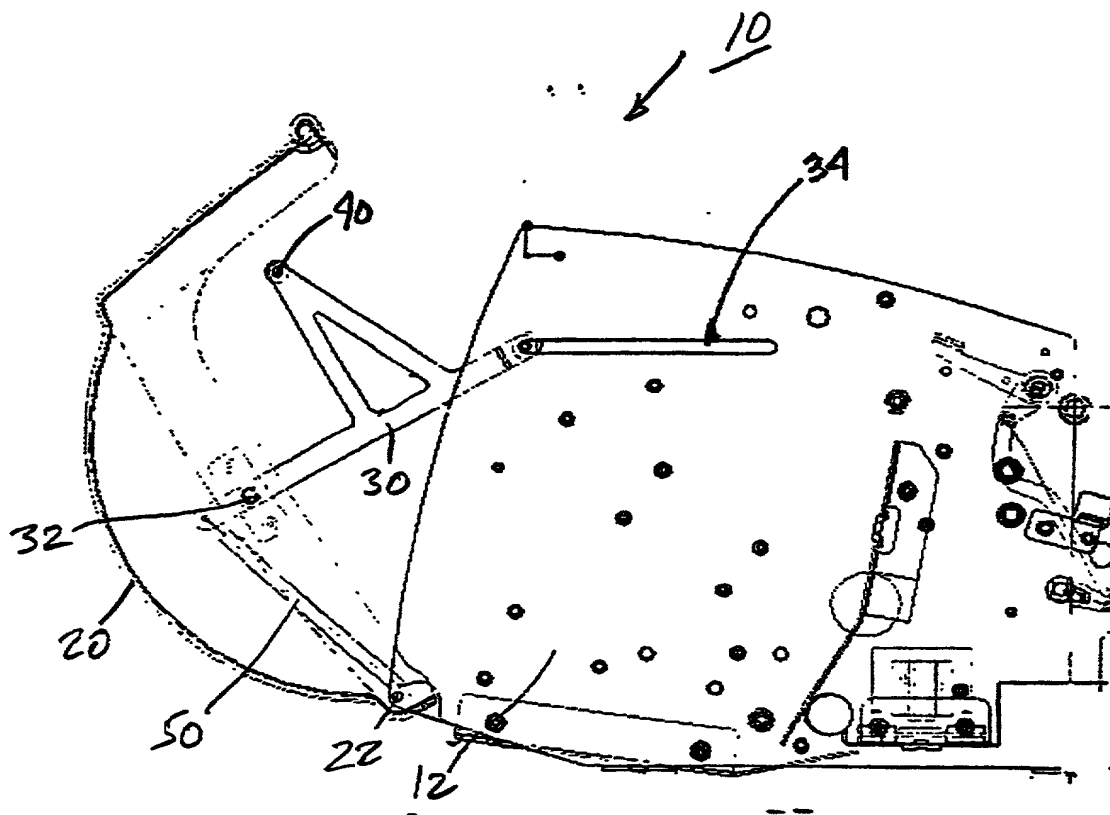


FIG 24